

Application Serial No.: 09/523,877
Filed: March 13, 2000

2. On page 1, immediately after the title, please insert the following text:

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10 3. On page 1, after the Copyright paragraph inserted per Item 2. above, but before the "Background of the Invention" section, please insert the following text:

--Related Applications

15 This application is related to co-pending U.S. Patent Application Serial No. 09/523,871 filed March 13, 2000 and entitled "Method and Apparatus for Jump Control in a Pipelined Processor", U.S. Patent Application Serial No. 09/524,179 filed March 13, 2000 and entitled "Method and Apparatus for Processor Pipeline Segmentation and Re-Assembly", U.S. Patent Application Serial No. 09/524,178 filed March 13, 2000 and entitled "Method and Apparatus for Loose Register Encoding Within a Pipelined Processor", and U.S. Patent Application No. 09/418,663 filed October 14, 1999, entitled "Method and Apparatus for Managing the Configuration and Functionality of a Semiconductor Design".--

25 4. On page 19, line 2 (after the Appendix A caption), please insert the following text:

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30 5. On page 39, line 2 (after the Appendix B caption), please insert the following text:

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